

# BigBrother-USB V1.0 - Pin assignment

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## Revisions

Version	Date	Changes
1.0	2012-08-19	First release of the document

## Pin assignment

Shield 0		
Pin	Assignment on the microcontroller	Comment
RST	RST	Reset
3.3V		3.3V power supply
5V		5V power supply
GND		Ground
GND		Ground
VIN		VIN input voltage
A0	PTB0	GPIO, Analog Input, SPI2 MISO
A1	PTB1	GPIO, Analog Input, SPI2 MOSI
A2	PTB2	GPIO, Analog Input, SPI2 Clock
A3	PTB3	GPIO, Analog Input, SPI2 Chip Select
A4	PTB4	GPIO, KBI, Analog Input
A5	PTB5	GPIO, KBI, Analog Input
D0	PTE1 and PTH0	GPIO, RxD1, SDA2
D1	PTE0 and PTH1	GPIO, TxD1, SCL2
D2	PTH2	GPIO, RGPIO
D3	PTH3	GPIO, RGPIO
D4	PTH4	GPIO, RGPIO
D5	PTF4	GPIO, TPM2CH0
D6	PTF5	GPIO, TPM2CH1
D7	PTG3	GPIO, KBI
D8	PTJ3 and IRQ	GPIO, RGPIO, IRQ
D9	PTJ4	GPIO, RGPIO
D10	PTE7	GPIO, SPI1 Chip Select
D11	PTE5	GPIO, SPI1 MOSI
D12	PTE4	GPIO, SPI1 MISO
D13	PTE6	GPIO, SPI1 Clock
AREF	VDDA/VREFH	Reference voltage for analog inputs, connected to 5V power supply
Shield 1		
Pin	Assignment on the microcontroller	Comment
RST	RST	Reset
3.3V		3.3V power supply
5V		5V power supply
GND		Ground
GND		Ground
VIN		VIN input voltage
A100	PTB6	GPIO, Analog Input
A101	PTB7	GPIO, Analog Input
A102	PTD0	GPIO, Analog Input, ACMP+
A103	PTD1	GPIO, Analog Input, ACMP-
A104	PTD3 and PTD2	GPIO, KBI, Analog Input, ACMPO

A105	PTD4 and PTG2	GPIO, KBI, Analog Input
D100	PTC5 and PTC1	GPIO, RxD2, SDA1
D101	PTC3 and PTC0	GPIO, TxD2, SCL1
D102	PTJ0	GPIO, RGPIO
D103	PTE2	GPIO, TPM1CH0
D104	PTJ1	GPIO, RGPIO
D105	PTE3	GPIO, TPM1CH1
D106	PTF0	GPIO, TPM1CH2
D107	PTG0	GPIO, KBI
D108	PTG1	GPIO, KBI
D109	PTF1	GPIO, TPM1CH3
D110	PTF2	GPIO, TPM1CH4
D111	PTF3	GPIO, TPM1CH5
D112	PTC2	GPIO, IRO
D113	PTJ2	GPIO, RGPIO
AREF	VDDA/VREFH	Reference voltage for analog inputs, connected to 5V power supply